

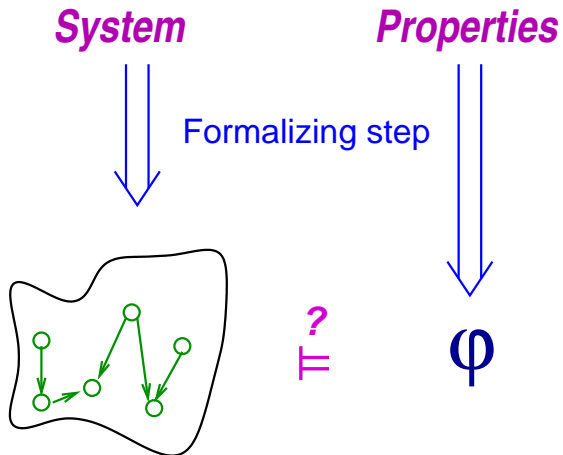
Models for Efficient Timed Verification

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LSV / ENS de Cachan – CNRS UMR 8643

Monterey Workshop - “Composition of embedded systems”

Model checking



Model checking

We want. . .

- **Expressive models:** communication, variables, timing constraints, probabilities. . .
- **Expressive specification languages:** natural, powerful, intuitive. . .

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- **Expressive models:** communication, variables, timing constraints, probabilities. . .
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AND EFFICIENT ALGORITHM !

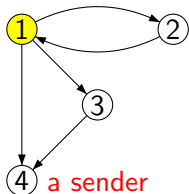
Main limit of model checking = state explosion problem

State explosion problem

An example: a “protocol”...

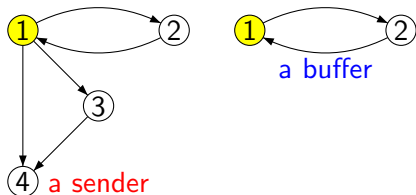
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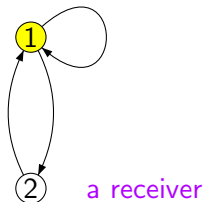
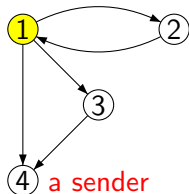
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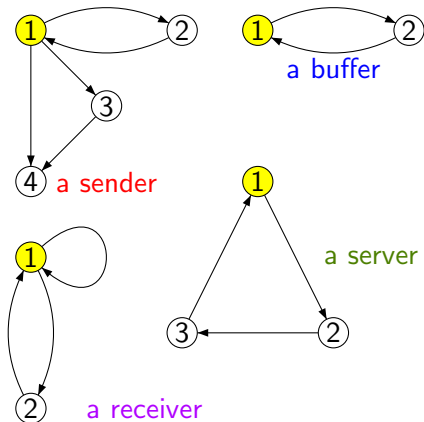
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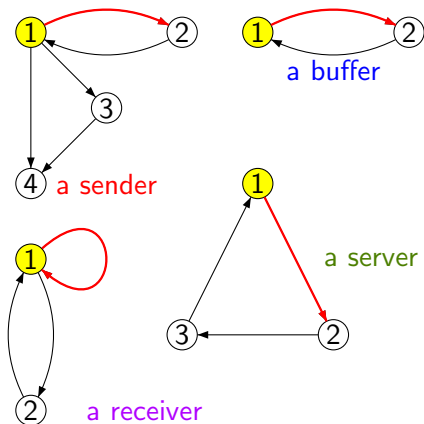
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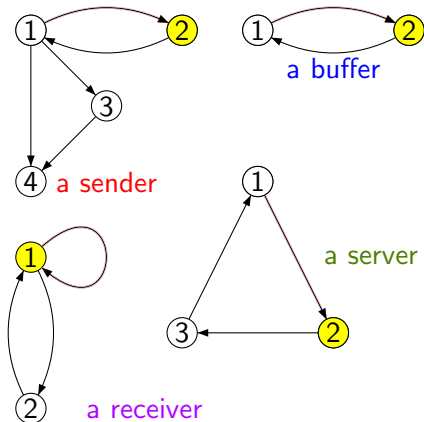
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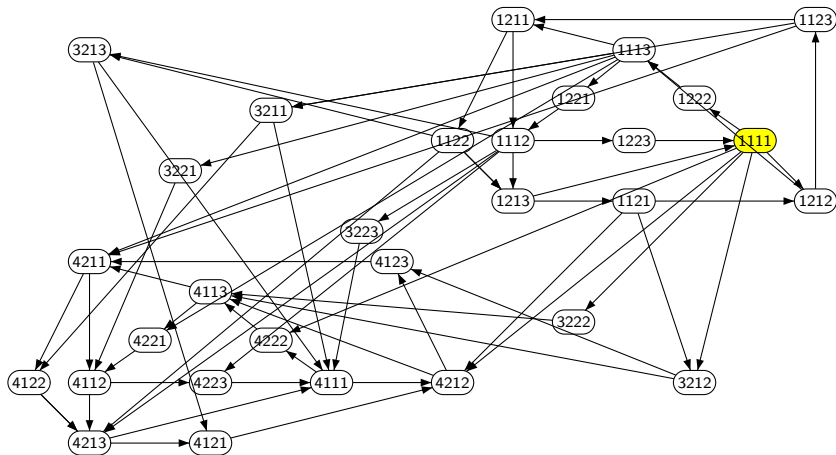


State explosion problem

An example: a “protocol”...



State explosion problem



State explosion problem

This has motivated

- symbolic methods
- heuristics:
 - on-the-fly algorithms
 - acceleration
 - partial order
 - ...
- abstraction
- ...

Model checkers exist and they work rather nicely !

Verification of compositions of embedded systems

Two difficulties:

- the **composition**...
- Usually we need to address verification of **quantitative aspects**:
 - timing constraints (**Real-time systems**)
 - probabilities
 - costs
 - data
 - ...

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 - probabilities
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 - data
 - ...

Each one may induce a complexity blow-up.

This can be measured by the structural complexity of verification problems: for ex. the composition and the state explosion problem.

State explosion problem

This state explosion holds for **synchronized systems**, **systems with boolean variables**, **1-safe Petri nets** etc.

→ These are **“non-flat” systems**.

In practice, a model S is described as a non-flat system (whose operational semantics is a “flat” transition system T)

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Complexity of model checking non-flat systems

Model checking	$T \models \Phi$	“non-flat syst.” $\models \Phi$
Reachability	NLOGSPACE-C	PSPACE-C
CTL model checking	P-C	PSPACE-C
AF μ -calculus	P-C	EXPTIME-C

(Papadimitriou, Vardi, Kupferman, Wolper, Rabinovich, . . .)

Challenge

Define new formalisms to model and verify the embedded systems, with. . .

- timing constraints
- probabilities
- costs, dynamic variables
- . . .
- extended specification languages

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 - request \Rightarrow AF grant
 - request \Rightarrow $AF_{<200}$ grant
 - request \Rightarrow $\mathbb{P}_{>0.9}F_{<200}$ grant
 - request \Rightarrow $\langle \text{Agt}_1 \rangle \mathbb{P}_{>0.9}F_{<200}$ grant

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without increasing (too much) the complexity !

Efficient timed model-checking

obj: find timed models and timed specification languages for which verification can be done efficiently.

→ To model simply timed systems

→ To use to abstract complex timed systems

→ To combine with other quantitative extensions

Outline

- 1 Timed specification languages
- 2 Timed models
 - Discrete-time models
 - Timed automata
- 3 Conclusion

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Classical verification problems

- **Reachability of a control state**
- $\mathcal{S} \sim \mathcal{S}' ?$: (bi)simulation, etc.
- $L(\mathcal{S}) \subseteq L(\mathcal{S}') ?$: language inclusion
- $(\mathcal{S} | A_T)$ + reachability : testing automata
- $\mathcal{S} \models \Phi$ with Φ a temporal logic formula : **model checking**

Timed properties

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“Any problem is followed by an alarm”

With CTL: $AG \left(\text{problem} \Rightarrow AF \text{ alarm} \right)$

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Timed properties:

“Any problem is followed by an alarm in at most 10 time units”

With TCTL:

$AG \left(\text{problem} \Rightarrow AF_{\leq 10} \text{ alarm} \right)$

Timed CTL

We use *TCTL* whose formulae are built from:

- atomic propositions in Prop (For ex. *Alarm*, *Problem*, ...)
- boolean combinators (\wedge , \vee , \neg), and
- temporal operators tagged with timing constraints: $E _ U_{\sim c}$ and $A _ U_{\sim c}$ with $\sim \in \{<, \leq, =, \geq, >\}$ and $c \in \mathbb{N}$.

+ all the standard abbreviations: $AG_{\sim c}$, $AF_{\sim c}$ etc.

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+ all the standard abbreviations: $AG_{\sim c}$, $AF_{\sim c}$ etc.

$$s \models E\varphi U_{\sim c}\psi \Leftrightarrow \exists \rho = \rho' \cdot \rho'' \in Exec(s) \text{ with } s \xrightarrow{\rho'} s' \text{ and } \\ \text{Time}(\rho') \sim c \text{ and } s' \models \psi, \\ \text{and } s'' \models \varphi \text{ for all } s <_{\rho} s'' <_{\rho} s'$$

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Studying timing constraints & complexity

Two problems:

- 1 Are there simpler models – with discrete time – for which model checking can be efficient ?
- 2 Are there classes of Timed Automata which allow efficient model checking ?

Timed transition systems

= a transition system + a **notion of time**

⇒ used to define the semantics of real-time systems

Let \mathbb{T} be a time domain: \mathbb{N} , \mathbb{R}_+ or \mathbb{Q}_+ .

Timed transition system

$$\mathcal{T} = \langle S, s_0, \rightarrow, I \rangle$$

- S is an (possibly infinite) set of states, $s_0 \in S$
- $\rightarrow \subseteq S \times \mathbb{T} \times S$
- $I : S \rightarrow 2^{\text{Prop}}$: assigns atomic propositions to states

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Every finite run σ in \mathcal{T} has a finite duration $\text{Time}(\sigma)$.

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Simple models for timed verification

It is possible to use **classical Kripke structures** as timed models.

There is **no inherent concept of time**: time elapsing is encoded by events.

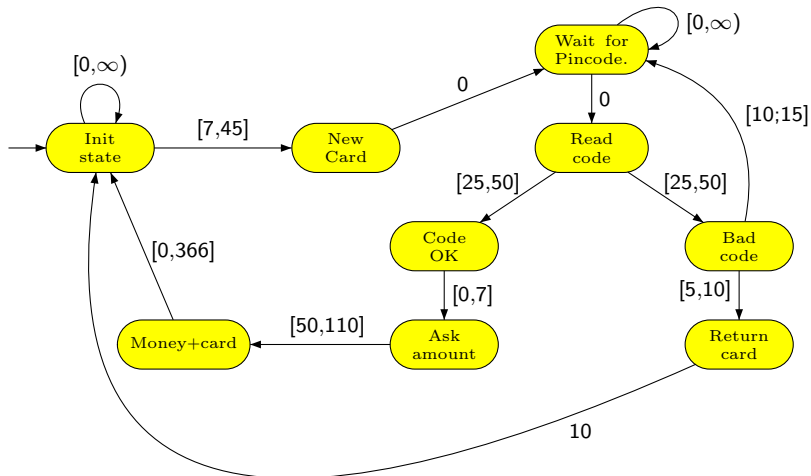
For example:

- each transition = one time unit (Emerson et al.)
- or: a “tick” proposition labels states where one t.u. elapses.

Timed model checking can be efficient (polynomial-time) !

Durational Transition Graphs extend these models without losing efficiency.

Durational Transition Graph



Durational Transition Graph

$$\mathbf{T} = \mathbb{N}$$

A durational transition graph S is $\langle Q, R, I \rangle$ where

- Q is a (finite) set of *states*,
- $R \subseteq Q \times \mathcal{I} \times Q$ is a total *transition relation with duration*
- $I : Q \rightarrow 2^{\text{Prop}}$ labels every state with a subset of **Prop**.

\mathcal{I} = set of intervals “[n, m]” or “[n, ∞)” (with $n, m \in \mathbb{N}$)

$q \xrightarrow{[n,m]} q'$: “moving from q to q' takes some duration d in $[n, m]$.”

Two semantics are possible. . .

Two semantics for DTGs

Consider the run: `Init_State` $\xrightarrow{15}$ `New_Card` $\xrightarrow{0}$ `Wait_for_Pin C.` $\xrightarrow{20}$...

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- Jump semantics:

- date: 0 \rightsquigarrow Init_State
- date: 15 \rightsquigarrow New_Card
- ...

no intermediary states !

⊕ Simple semantics, no extra states.

⊖ Not always natural. Difficult to synchronize two DTGs.

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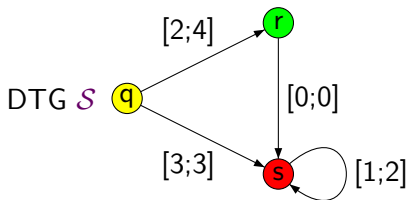
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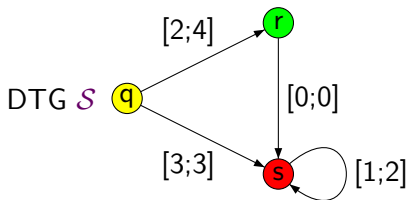
- Continuous semantics:

- date: 0...14 \rightsquigarrow `Init_state`
- date: 15 \rightsquigarrow `New_card`
- date: 15...34 \rightsquigarrow `Wait_for_Pin_C`
- ...

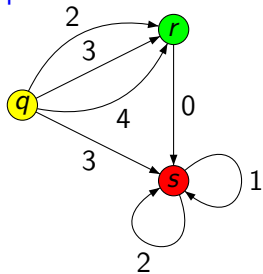
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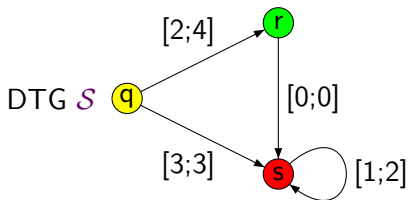
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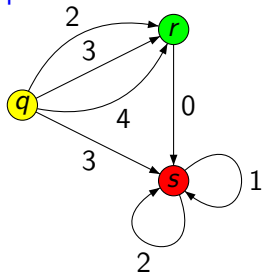
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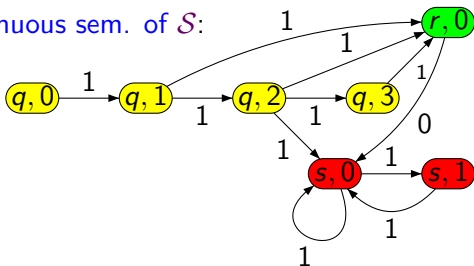
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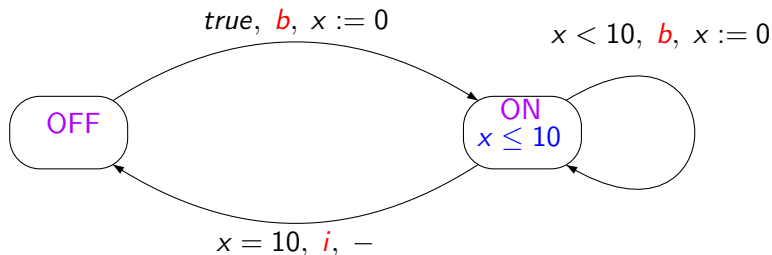
(q, i) : “ i time units have already been spent in q ”

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Timed Automata – Example

\mathcal{A} = an automaton (locations and transitions) + clocks



Timed automata - definition [Alur & Dill]

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Clocks progress synchronously with time (Time domain = \mathbb{R}_+)

Transitions: $l \xrightarrow{g,a,r} l' \in T$ with:

- g is the guard,
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▲ **Action transition**:

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▲ **Delay transition**:

$$\forall t \in \mathbb{R}_+, (l, v) \xrightarrow{t} (l, v + t) \text{ iff } \forall 0 \leq t' \leq t, v + t' \models \text{Inv}(l)$$

Model checking for TA

\mathcal{A} defines an **infinite** timed transition system.

Decision procedures are based on the **region graph** technique (Alur, Courcoubetis, Dill)

From \mathcal{A} and Φ , one defines an equivalence $\equiv_{\mathcal{A},\Phi}$ s.t.:

- $v \equiv_{\mathcal{A},\Phi} v' \Rightarrow \left((l, v) \models \Phi \text{ iff } (l, v') \models \Phi \right)$
- $\mathbb{R}_+^X / \equiv_{\mathcal{A},\Phi}$ is finite

A region = An equivalence class of $\equiv_{\mathcal{A},\Phi}$

We can reduce $\mathcal{A} \models \Phi$ to $\left(\mathcal{A} \times \mathbb{R}_+^X / \equiv_{\mathcal{A},\Phi} \right) \models \Phi$
... and use a standard model checking algorithm.

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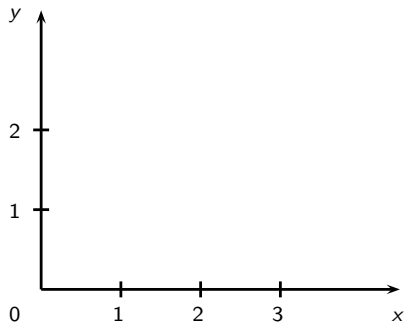
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! The size of $\mathbb{R}_+^X / \equiv_{\mathcal{A},\Phi}$ is in $O(|X|! \cdot M^{|X|})$ **!**

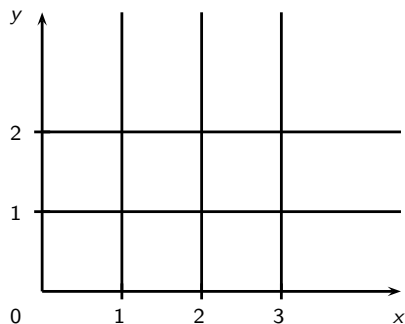
The region abstraction



$$X = \{x, y\}$$

$$M_x = 3 \text{ and } M_y = 2$$

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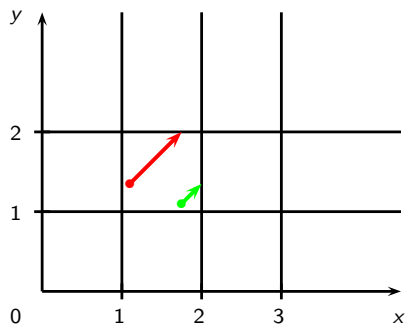


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- “compatibility” between regions and constraints $x \sim k$ and $y \sim k$

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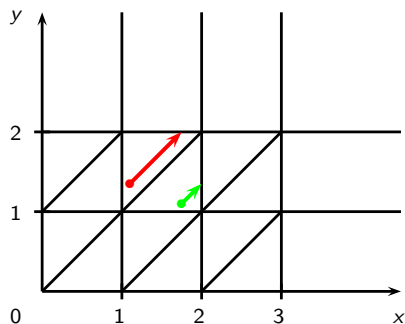


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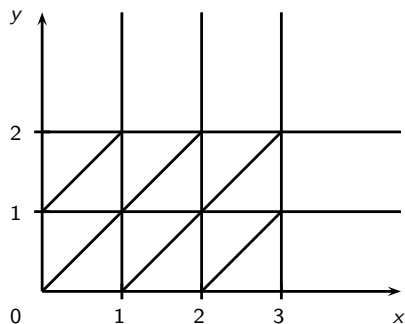


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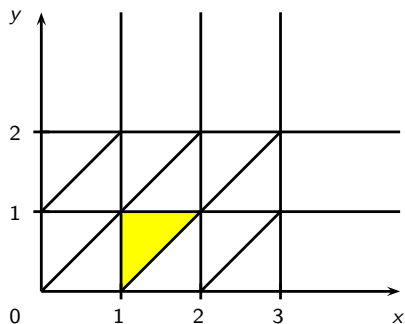


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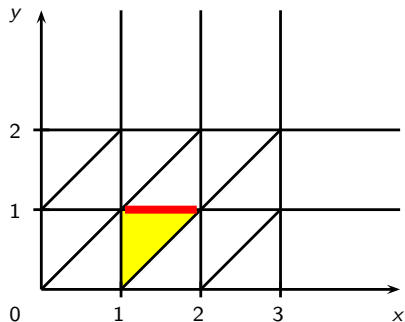
region defined by

$$I_x =]1; 2[, I_y =]0; 1[$$

$$\{x\} < \{y\}$$

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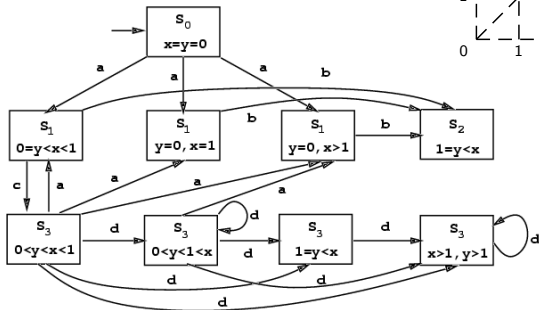
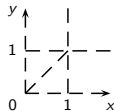
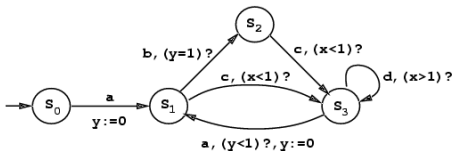
$$\{x\} < \{y\}$$

successor by delay transition:

$$I_x =]1; 2[, I_y = [2; 2]$$

- “compatibility” between regions and constraints $x \sim k$ and $y \sim k$
- “compatibility” between regions and time elapsing

An example [AD 90's]



Complexity of timed verification

- Reachability in TA is **PSPACE-C** (Alur & Dill)
- Reachability in TA with **three clocks** is **PSPACE-C** (Courcoubetis & Yannakakis)
- Reachability in TA with **constants in $\{0, 1\}$** is **PSPACE-C** (Courcoubetis & Yannakakis)
- Model checking **Timed CTL** over TA is **PSPACE-C** (Alur, Courcoubetis & Dill)
- Model checking **AF Timed μ -calculus** over TA is **EXPTIME-C** (Aceto & Laroussinie)

(But tools (Kronos, UppAal) exist and have been applied successfully for verifying industrial case studies.)

One/two clock timed automata

[LMS2004]

- 1 Model checking $TCTL$ on 1C-TA is PSPACE-complete.
- 2 Reachability in 1C-TA is NLOGSPACE-complete.
- 3 Model checking $TCTL_{\leq, \geq}$ on 1C-TA is P-complete.

- 1 Reachability in 2C-TA is NP-hard.
- 2 Model checking CTL on 2C-TA is PSPACE-complete.

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Conclusion

	discrete time			dense time		
	DTG $\xrightarrow{0/1}$ [LST03]	DTG [LMS06] jump cont.		1C-TA [LMS04]	2C-TA [LMS04]	TA [ACD93] [CY92]
Reachability	NLOGSPACE-C				NP-hard	PSPACE-C
$TCTL_{\leq, \geq}$	P-complete				PSPACE-C	
$TCTL$	P-compl.	Δ_2^P	PSPACE-complete			
$TCTL_c$	PSPACE-complete					

Conclusion

- No efficient algorithm for " $TCTL$ +clocks".
- As soon as there are two clocks, a complexity blow-up occurs for any verification problem.
- For $TCTL$, model-checking may be efficient only for the simple DTGs with durations in $\{0, 1\}$.
- For $TCTL_{\leq, \geq}$, it is possible to have efficient model-checking algorithm for any kind of DTG and 1C-TA.
- The previous result can be extended to probabilistic timed systems (Probabilistic DTG and 1-clock Probabilistic TA).